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SEP 22 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application	)	PATENT APPLICATION
	)	
Inventors: Barth, et al.	)	
	)	Art Unit: 2187
Application No.: 09/559,835	)	
	)	Examiner: Nguyen, H.
Reissue of: U.S. Patent No. 5,765,020	)	
Issued June 9, 1998	)	
	)	
Filed: April 26, 2000	)	
	)	
Title: METHOD OF TRANSFERRING DATA BY	)	
TRANSMITTING LOWER ORDER AND UPPER	)	
ORDER MEMORY ADDRESS BITS IN SEPARATE	)	Customer No. 28554
WORDS WITH RESPECTIVE OP CODES AND	)	
START INFORMATION	)	

SUPPLEMENTAL DECLARATION FOR REISSUE APPLICATION UNDER 37 CFR 1.175

Dear Sir:

As below named inventors, we hereby declare that:

1. Our post office addresses and citizenship are stated below next to our names.
2. We verily believe that we are original, first and joint inventors of the subject matter which is described and claimed in the above-identified reissue application entitled "Method Of Transferring Data By Transmitting Lower Order And Upper Order Memory Address Bits In Separate Words With Respective OP Codes And Start Information" (herein "the reissue application").
3. We have reviewed and understand the contents of the reissue application, including the claims, as amended by the amendments as submitted.
4. We acknowledge the duty to disclose information material to the patentability of the reissue application in accordance with 37 CFR 1.56.

- 1 -

Attorney Docket No.: RAMB-01058US0  
ramb/1058/1058 supp declaration.doc

5. Every error which was corrected in the present reissue application, and is not covered by a prior oath/declaration submitted in this application, arose without any deceptive intention on the part of the applicant.

We further declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Date: 8-1-05

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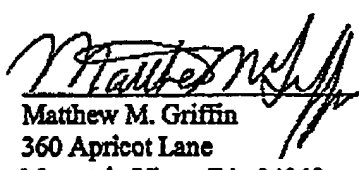
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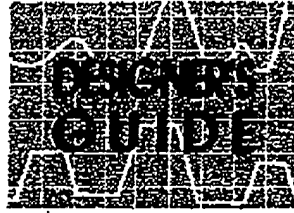
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dynamic RAMs  
Part 1

# Unraveling the intricacies of dynamic RAMs

MP7

MP7

